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# TRANSMITTAL OF FORMAL DRAWINGS

Docket No.  
YOR920030418US1

In Re Application Of: **Darringer et al.**

Serial No.

Filing Date

Confirmation No.

Examiner

Art Unit

10/714,750

November 17, 2003

Not yet assigned

2123

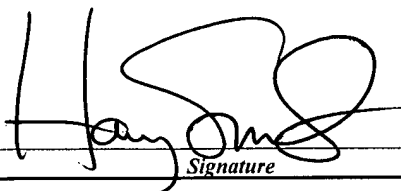
Invention: **Method, System and Computer Program Product for Implementing Uncertainty in Integrated Circuit Designs with Programmable Logic**

Address to:  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Transmitted herewith are:

8 sheets of formal drawing(s) for this application.

☐ Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).

  
Signature

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203-925-9400

Dated: 6/22/2004

I certify that this document and attached formal drawings are being deposited on 6/22/04 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



Signature of Person Mailing Correspondence

Ann Okrentowich

Typed or Printed Name of Person Mailing Correspondence